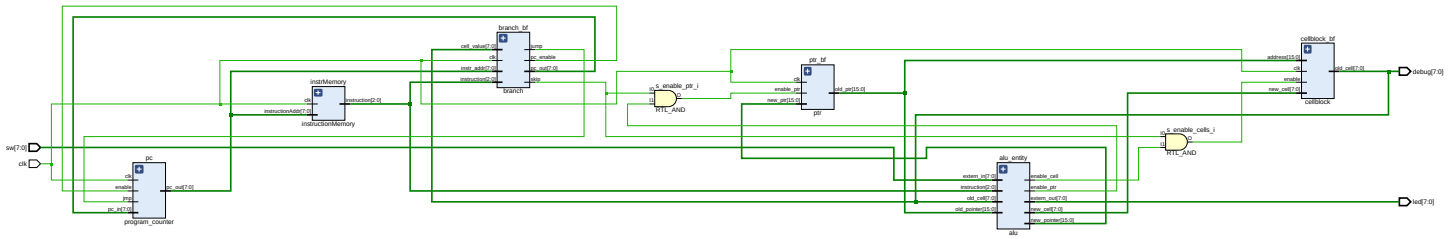
sw[7:0]
clk
pc
clk
enable
PS
pc_in[7:0]
pc_out[7:0]
program_counter
instrMemory
clk
instructionAddr[7:0]
instruction[2:0]
instructionMemory
branch_bf
cell_value[7:0]
clk
instr_addr[7:0]
instruction[2:0]
jump
pc_enable
pc_out[7:0]
step
branch
s_enable_ptr_i
RTL_AND
ptr_bf
clk
enable_ptr
new_ptr[15:0]
old_ptr[15:0]
ptr
alu_entity
extern_in[7:0]
instruction[2:0]
old_cell[7:0]
old_pointer[15:0]
enable_cell
enable_ptr
extern_out[7:0]
new_cell[7:0]
new_pointer[15:0]
alu
s_enable_cells_i
RTL_AND
cellblock_bf
address[15:0]
clk
enable
new_cell[7:0]
old_cell[7:0]
cellblock
debug[7:0]
led[7:0]